

**METHOD OF FABRICATING AN INTEGRATED CIRCUIT THROUGH  
UTILIZING METAL LAYERS TO PROGRAM RANDOMLY POSITIONED  
BASIC UNITS**

Appl. No. : 10/708,256 Confirmation No. 2255  
Applicant : Hsin-Shih Wang  
Filed : February 19, 2004  
TC/A.U. : 2825  
Examiner : Siek, Vuthe  
Docket No. : FTCP0031USA0  
Customer No. : 27765

Commissioner for Patents

P.O. Box 1450

Alexandria VA 22313-1450

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**AMENDMENT**

Sir:

In response to the Office action of June 06, 2006, please amend the above-identified application as follows:

10      **Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 7 of this paper.